

Introduction

CL12812M8RIP5000 is an ideal means to link Camera Modules or CMOS Image Sensor (CIS) to ISP (Imaging Signal Processor) and DSP. CL12812M8RIP5000 is designed to support data rate up to 5Gbps utilizing SLVS-EC ver.2.0 interface specification.

Feature

- SLVS-EC ver.2.0 compliant
- Supporting for Differential Input Signals
 - SLVS-EC Maximum 5.0Gbps
- Xtal Input Clock Frequency Selectable
 - 25MHz /50MHz / 75MHz @ 5Gbps (2.5Gbps, 1.250Gbps)
 - 37.125MHz /54MHz @ 4.752Gbps (2.376Gbps, 1.188Gbps)
 - 24MHz /48MHz / 72MHz @ 4.608Gbps (2.304Gbps, 1.152Gbps)
- Maximum Output Clock Frequency
 - ~250MHz @ Select parallel data bus width to 20bit
- Power Supply:
 - Vcc=1.8V (IO and Analog) Vdd=0.75 V (Inside Core) @ 6nFFC
 - Vcc=1.8V (IO and Analog) Vdd=0.8 V (Inside Core) @12nFFC
- Maximum Number of Lanes : 8 Lanes
- 20-bit/Lane Parallel Outputs (SLVS-EC)
- Including Power Down Mode
- Including Differential Low Detect Circuit for SLVS-EC
- TSMC 6nm FF Process 1P13M_1X_h_1Xa_v_1Ya_h_5Y_vhvhv_2Yy2Z
TSMC 124nm FF Process 1P11M_2Xa1Xd_h_3Xe_VHV_2Y2R
IO Direction: South-North

Support Link controller

- CD12812 Link controller (Ready)

We can provide process porting and lane customization.
If you have any questions or requirements,
such as a detailed information of Curious IP products,
please contact us at the email address : curious.info@curious-jp.com