

## **Introduction**

The CL12811I4T1ES1HIP2500 is an ideal means to link Camera Modules or CMOS Image Sensor (CIS) to Host System.

The CL12811I4T1ES1HIP2500 converts the input parallel data to the serial data and output it.

The CL12811I4T1ES1HIP2500 is designed to support maximum 2.5Gbps data rate utilizing SLVS-MI.

## **Feature**

- SLVS-MI compliant
- Supporting for two kind Differential Output Signals: SLVS-MI (Maximum 2.5Gbps)
- Input Clock Frequency: (SLVS-MI 10bit SER) PCK\_N= ~250MHz
- Power Supply : 2.8V, 1.2V(PLL/BGR) 1.2V(PHY)
- Max TX Lane Number: 4-Lane
- Data Input Path: SLVS-MI (10bit Parallel)
- Include Power Down Mode
- Include Output clock's On/Off Mode
- Include Dp, Dm polarity Control
- TPSCo 65nm BSB Process
  - Triple well structure Layer: 7M/1L (1M~ 6 M, 9M, L) 1.2V / 3.3V Transistor
- Various process porting support available (Please contact us.)
- Supporting Link-layer (Soft Macro): CD12811

If you have any questions or requirements, such as a detailed information of Curious IP products, please feel free to contact us at the email address : [curious.info@curious-jp.com](mailto:curious.info@curious-jp.com)