

Introduction

The CL11092IP270 is RSDS ReceiverAMP optimized for high data rate and low power applications. The CL11092IP270 is designed to support data rates in excess of 270Mbps utilizing Reduced Swing Voltage Differential Signaling (RSDS) technology. The CL11092IP270 accept low voltage (200mV typical) differential input signals and translates them to LVCMOS output levels.

Feature

- Max 270Mbps switching rates
- RSDS “Intra-panel” Interface Rev.1.0 Compliant
- Low power single 3.3V (Option: 2.5 / 2.8V) (Option: 1.0 / 1.2 / 1.5 / 1.8V Logic/Level Shifter)
- Power Down Mode

Block Diagram

