

Introduction

The CL11022IP680 is mini-LVDS ReceiverAMP optimized for high data rate and low power applications. The CL11022IP680 is designed to support data rates in excess of 680Mbps utilizing mini Low Voltage Differential Signaling (mini-LVDS) technology. The CL11022IP680 accept low voltage (200mV typical) differential input signals and translates them to LVCMOS output levels.

Feature

- Max 680Mbps (max: 896Mbps) switching rates
- Low power single 3.3V (Option: 2.5 / 2.8V) (Option: 1 / 1.2 / 1.5 / 1.8V Logic/Level Shifter)
- Supports Fail-Safe function to all input channels
- Power Down Mode

Block Diagram

