

Introduction

The CL11033IP2000 is an ideal means to link mobile camera modules to baseband processors and baseband processors to LCD panels. The CL11033IP2000 is designed to support data rate in excess of 2Gbps utilizing MIPI-DPHY specification. The differential outputs provide low EMI with its typical low output swing of 200mV.

Feature

- MIPI Alliance Specification for DPHY v1.00.00 compliant
- Maximum 2Gbps switching rate
- Low Power single 1.2V (Option: 1.5 / 1.8 / 2.8 / 3.3V)
- 200mV swing MIPI-DPHY for low EMI
- Power Down mode < 1μA

Block Diagram

