

Introduction

The CL11002IP1200 is LVDS ReceiverAMP optimized for high data rate and low power applications. The CL11002IP1200 is designed to support data rates in excess of 1.2Gbps utilizing Low Voltage Differential Signaling (LVDS) technology. The CL11002IP1200 accept low voltage (345mV typical) differential input signals and translates them to LVCMOS output levels.

Feature

- Max 1.2Gbps switching rates
- Low power single 3.3V (Option: 2.8V) (Option: 1.0 / 1.2 / 1.8V Logic/Level Shifter)
- TIA/EIA-644 and IEEE 1596.3 compliant
- Power Down Mode
- Supports Fail-Safe function to all input channels

Block Diagram
